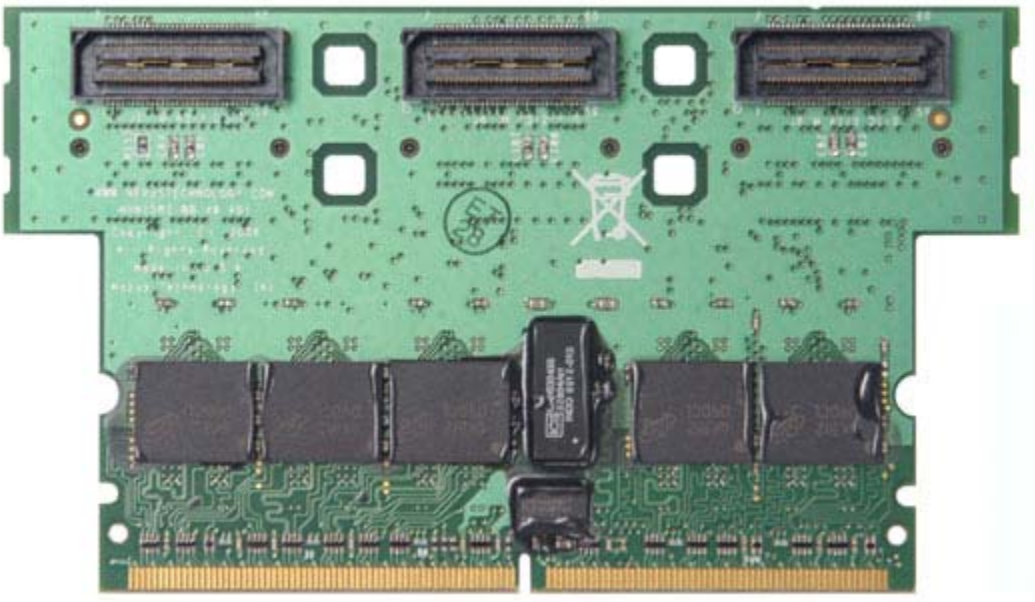
DDRII, 667MHz NEXVu miniDIMM & Analysis Software

miniDIMM DDRII NEXVu



- DDRII miniDIMM designed to JEDEC standards with Tektronix logic analyzer connections
- Innovative approach provides visibility of actual eye patterns, as seen by the memory components
- Micron Technology memory components included with each miniDIMM
- miniDIMMs without memory components for use with alternate vendor memories are also available
- Uses new Low-Profile probes from Nexus for increased signal integrity
- Acquisition of DDRII Address/Command, Read and Write data
- Quick and easy connection between the DDRII bus and a Tektronix logic analyzer
- Selective Clocking – Filter *Refresh* and *Idle* cycles for improved TLA memory utilization
- 8GHz timing analysis on every channel
- Correlate data with data from other acquisition modules

General Description

The DDRII miniDIMM NEXVu allows for the acquisition of Address/Command, Read and Write data of DDRII at 667MHz or slower. Product comes with Micron memory components and is also available without memory components for alternate vendor components to be installed.

Predefined Symbols for the following Command Cycles allow for easy trigger setup:

- Read Col Address Read
- Write Col Address Write
- Mode Register Set (MRS)
- Row Address Strobe
- Precharge
- Precharge Select Bank
- No Operation
- Ignore Command Data
- Burst Stop
- Refresh

Oscilloscope Connectivity to any channel without having to re-probe via the Tektronix logic analyzer's Enhanced iView Analog Mux capability.

8GHz MagniVu Timing on every channel of the logic analyzer

Selective Clocking stores data when commands are present and for 13 clock cycles after *Column Address* assertion resulting in fewer *Idle* cycles being stored in acquisition memory.

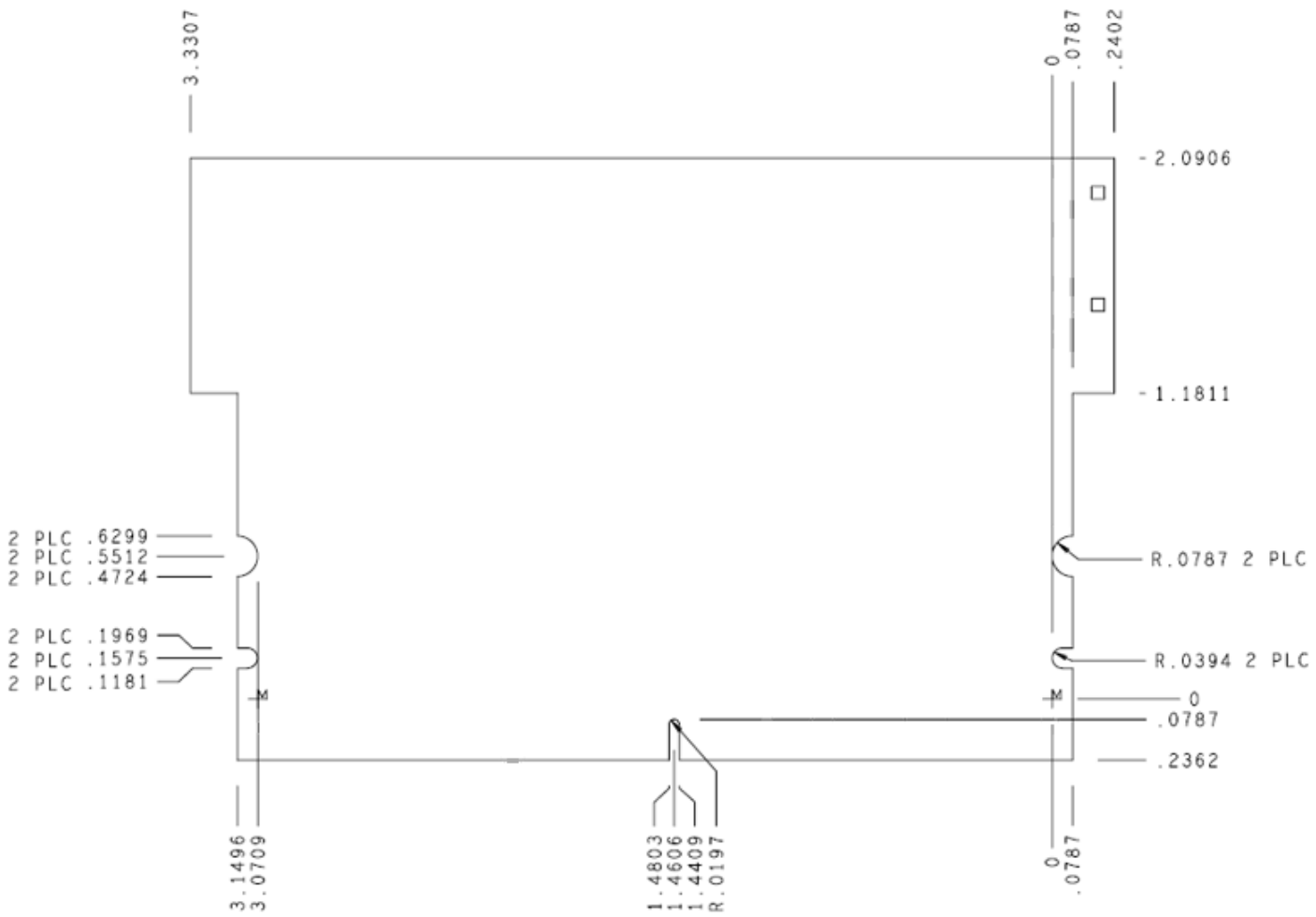
No Dedicated Slot – Does not require a dedicated slot

Logic Analyzer Support / Configuration

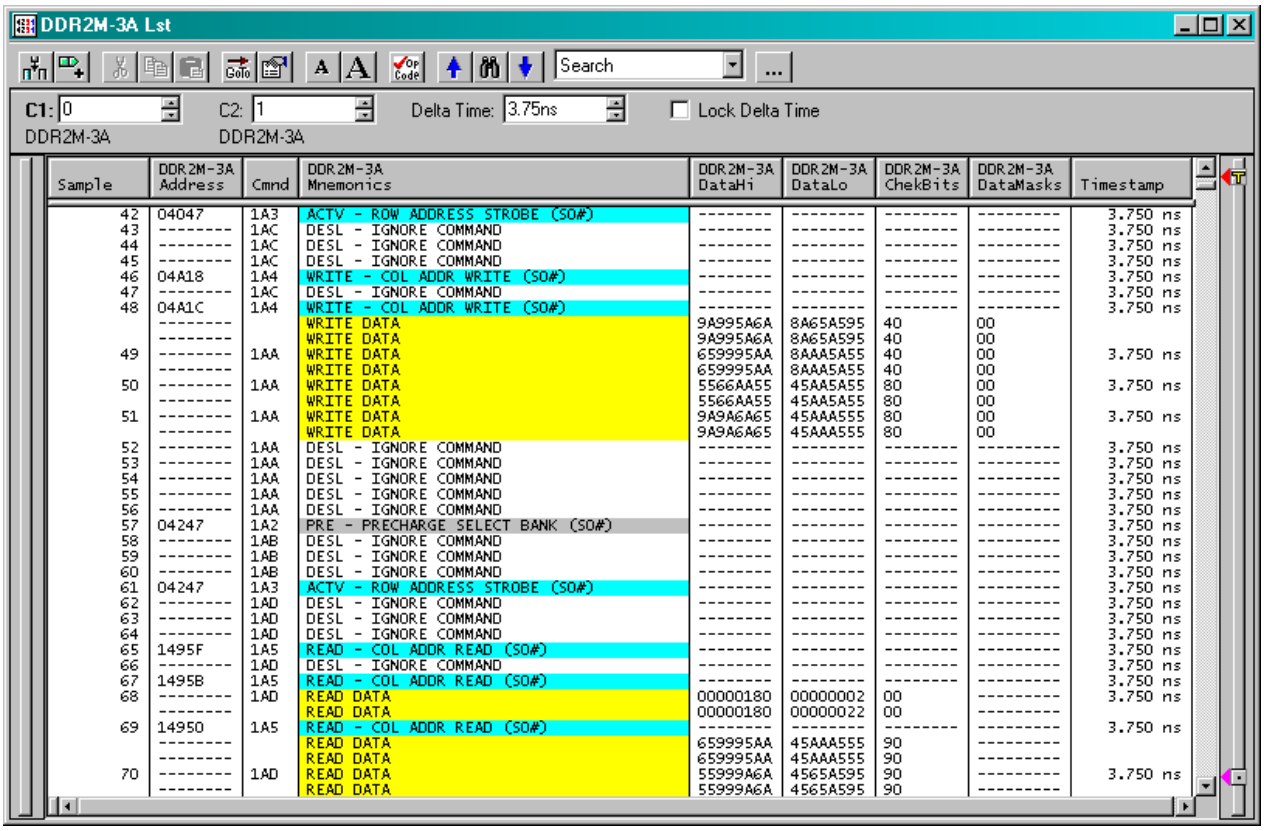
The following hardware is required to use this product.

A TLA7000 series or TLA700 series mainframe logic analyzer
- TLA Application Software Version 4.2 or newer
- Three (3) TLA7Ax4 acquisition modules with the 450MHz state speed option
- Four (4) NEX-PRB1X probes
- Two (2) NEX-PRB4X probes

Mechanical Outline

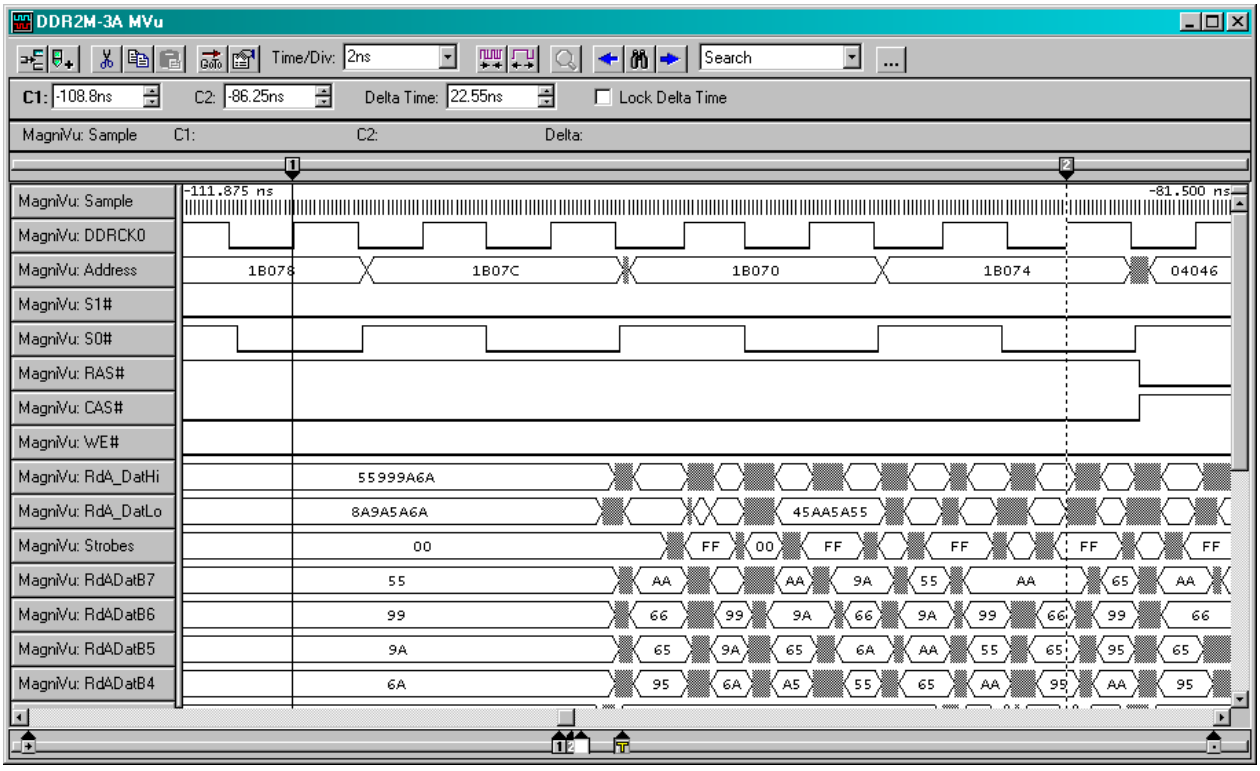


State Display



Sample	DDR2M-3A Address	Cmnd	DDR2M-3A Mnemonics	DDR2M-3A DataHi	DDR2M-3A DataLo	DDR2M-3A ChekBits	DDR2M-3A DataMasks	Timestamp
42	04047	1A3	ACTV - ROW ADDRESS STROBE (SO#)	--------	--------	--------	--------	3.750 ns
43	--------	1AC	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
44	--------	1AC	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
45	--------	1AC	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
46	04A18	1A4	WRITE - COL ADDR WRITE (SO#)	--------	--------	--------	--------	3.750 ns
47	--------	1AC	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
48	04A1C	1A4	WRITE - COL ADDR WRITE (SO#)	--------	--------	--------	--------	3.750 ns
	--------		WRITE DATA	9A995A6A	8A65A595	40	00	
	--------		WRITE DATA	9A995A6A	8A65A595	40	00	
49	--------	1AA	WRITE DATA	659995AA	8AAA5A55	40	00	3.750 ns
	--------		WRITE DATA	659995AA	8AAA5A55	40	00	
50	--------	1AA	WRITE DATA	5566AA55	45AA5A55	80	00	3.750 ns
	--------		WRITE DATA	5566AA55	45AA5A55	80	00	
51	--------	1AA	WRITE DATA	9A9A6A65	45AAA555	80	00	3.750 ns
	--------		WRITE DATA	9A9A6A65	45AAA555	80	00	
52	--------	1AA	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
53	--------	1AA	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
54	--------	1AA	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
55	--------	1AA	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
56	--------	1AA	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
57	04247	1A2	PRE - PRECHARGE SELECT BANK (SO#)	--------	--------	--------	--------	3.750 ns
58	--------	1AB	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
59	--------	1AB	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
60	--------	1AB	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
61	04247	1A3	ACTV - ROW ADDRESS STROBE (SO#)	--------	--------	--------	--------	3.750 ns
62	--------	1AD	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
63	--------	1AD	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
64	--------	1AD	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
65	1495F	1A5	READ - COL ADDR READ (SO#)	--------	--------	--------	--------	3.750 ns
66	--------	1AD	DESL - IGNORE COMMAND	--------	--------	--------	--------	3.750 ns
67	1495B	1A5	READ - COL ADDR READ (SO#)	--------	--------	--------	--------	3.750 ns
68	--------	1AD	READ DATA	00000180	00000002	00	--------	3.750 ns
	--------		READ DATA	00000180	00000022	00	--------	
69	14950	1A5	READ - COL ADDR READ (SO#)	--------	--------	--------	--------	3.750 ns
	--------		READ DATA	659995AA	45AAA555	90	--------	
	--------		READ DATA	659995AA	45AAA555	90	--------	
70	--------	1AD	READ DATA	55999A6A	4565A595	90	--------	3.750 ns
	--------		READ DATA	55999A6A	4565A595	90	--------	

Timing Display



Ordering / Contact Information

Part Number	NEX-NVD2M667SRBUNDLE
Description	Compete miniDIMM support including NEXVu and probes.
Includes	DDRII NEXVu miniDIMM - 667MHz, Registered, x8, Single-Rank Four NEX-PRB1X Two NEX-PRB4X Support Software Sample Point Analysis Software (NEX-SPA) Manual

Part Number	NEX- NVD2M667x8SR512R
Description	NEXVu miniDIMM only.
Includes	DDRII NEXVu miniDIMM - 667MHz, Registered, x8, Single-Rank Support Software Sample Point Analysis Software (NEX-SPA) Manual

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Placing an Order

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