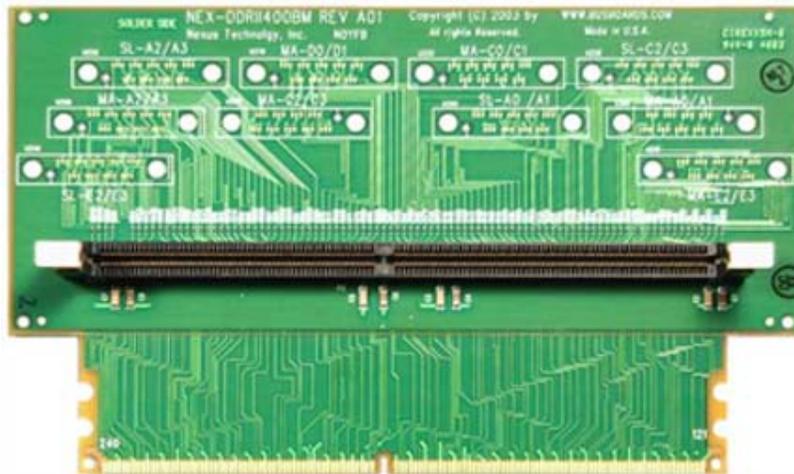# NEX-DDRII400BM

- Acquisition of DDRII 400MT/s Address/Command, Read and Write Data
- Mirror image of NEX-DDRII400 that enables simultaneous monitoring of two, adjacent DDRII DIMM slots
- Quick and easy connection between the DDRII bus and a Tektronix Logic Analyzer
- Extender design does not require a dedicated slot
- Simultaneous acquisition of DDRII Read and Write data
- Selective Clocking provides better utilization of logic analyzer memory by reducing acquisition of Refresh and Idle bus cycles
- Supports 240-pin, unbuffered or registered DDRII SDRAM DIMMs up to 400 MT/s
- Impedance controlled
- Matched trace length design
- No active buffering of the DDR signals
- Accurate 8GHz timing analysis on every channel of the TLA
- Simultaneous state and timing on every channel of the TLA
- Correlate DDRII data with data from other acquisition modules
- Use the TLA's Extended iView capabilities to view any channel on an oscilloscope without re-probing
- Software only support is available for embedded memory designs

General Description

NEX-DDRII400BM allows for the acquisition of Address/Command, Read and Write Data of 240-pin, unbuffered or registered DDRII SDRAM DIMMs up to 400 MT/s

8Ghz Timing Analysis available for all DDRII signals
.

Oscilloscope Connectivity on any channel without re-probing via the TLA's Enhanced iView Analog Mux capability

Selective Clocking stores data when commands are present and for 13 clock cycles after Column Address Assertion. This results in fewer Idle cycles being stored in acquisition memory.

Pre-Defined Symbols for the following Command Cycles allow for easy Trigger Setup:

- Read Col Address Read
- Write Col Address Write
- Mode Register Set
- Row Address Strobe
- Precharge

- Ignore Command Data
- Burst Stop
- Refresh
- Precharge Select Bank
- No Operation

No Dedicated Slot Required – The logic analyzer connects above the normal DIMM height so that there is no interference with adjacent DIMMs.

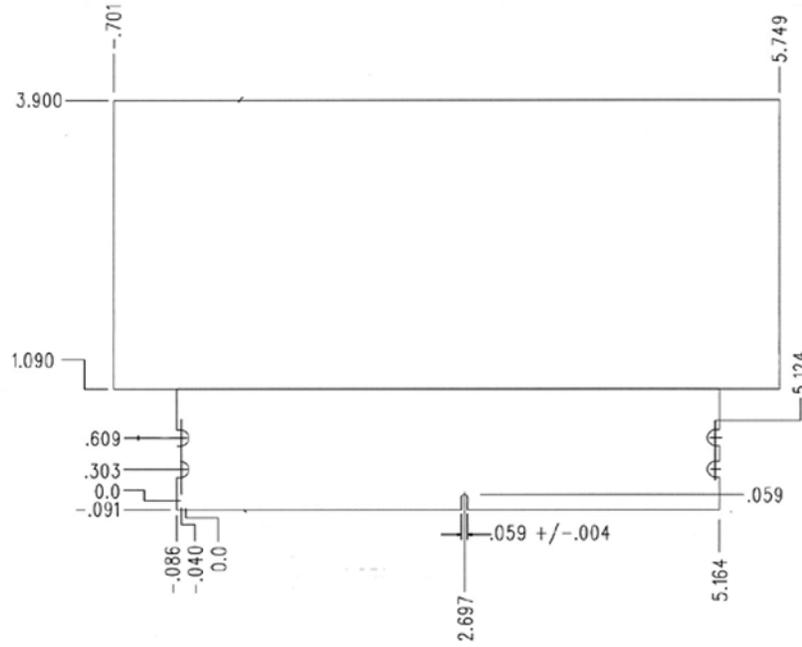The following support package(s) are included with this product:

> **MDDR2** allows the user to acquire both Read AND Write DDRII data from a target. This support requires 8-P6860 High-Density Compression probes.
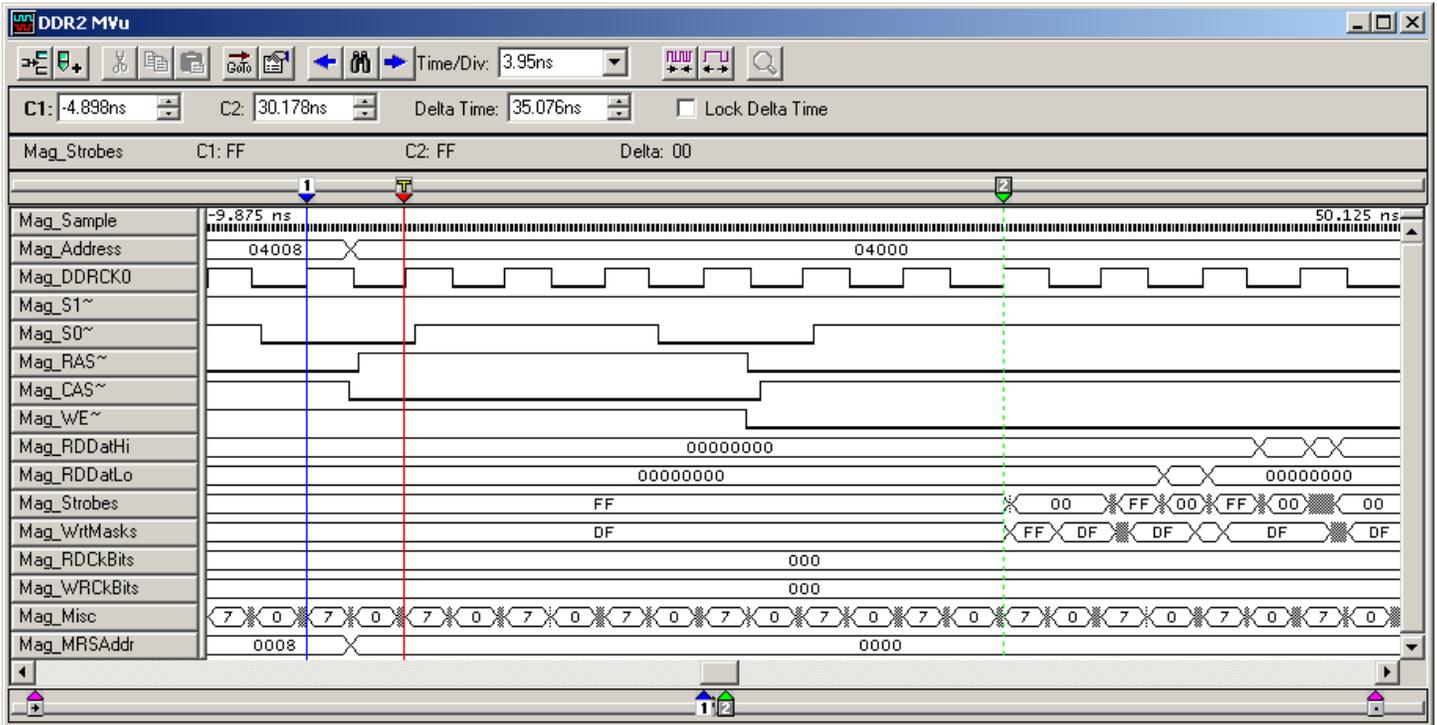
LA Support / Configuration

A TLA700 equipped with two, merged, 450MHz state speed acquisition modules (TLA7AA4 or TLA7AB4 cards) are required. When the upper Strobe/Write Mask signals are used as differential strobes (400DDR2S support) 1-P6880 and 7-P6860 probes are required. When the upper Strobe/Write Mask signals are used as Write Masks or single-ended strobes (400DDR2M support) 8-P6860 probes are required.

8GHz MagniVu Timing and Enhanced iView Analog Mux capabilities are available with either configuration.

Mechanical Outline

Timing Display

State Display

Software Mode, Address, Data and Command Cycles Only

Ordering / Contact Information

Part Number NEX-DDRII400BM
Includes: NEX- DDRII400BM adapter
             Software
             Manual

Additional software included:
     NEX-DDRSPA - TLA Software Plug-In for determining optimum Setup & Hold sample points.

Postal: Nexus Technology, Inc.
             78 Northeastern Blvd. #2
             Nashua, NH 03062

Telephone: 877-595-8116

Fax: 877-595-8118

Email: support@nexustechnology.com
             quotes@nexustechnology.com
             techsupport@nexustechnology.com

Website: www.nexustechnology.com

Placing an Order

Credit Card orders can be placed directly at 877-595-8116.
Purchase orders can be faxed to 877-595-8118.