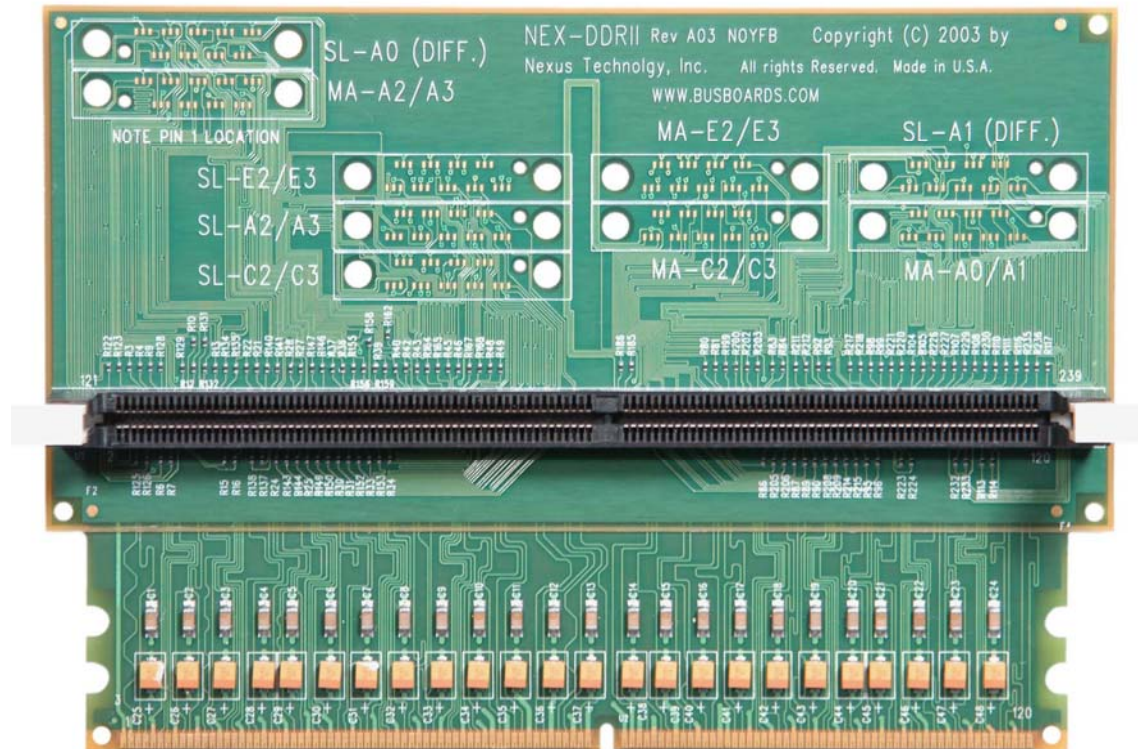


NEX-DDRII400



- Acquisition of DDRII 400 Address/Command, Read and Write Data
- Quick and easy connection between the DDRII bus and a Tektronix Logic Analyzer
- Extender design does not require a dedicated slot
- Simultaneous acquisition of DDRII Read and Write data
- Selective Clocking provides better utilization of logic analyzer memory by reducing acquisition of Refresh and Idle bus cycles
- Supports 240-pin, unbuffered or registered DDRII SDRAM DIMMs up to 400 MT/s
- Impedance controlled
- Matched trace length design
- No active buffering of the DDRII signals
- Accurate 8GHz timing analysis on every channel of the TLA
- Simultaneous state and timing on every channel of the TLA
- Correlate DDRII data with data from other acquisition modules
- Use the TLA's Extended iView capabilities to view any channel on an oscilloscope without re-probing
- Software only support is available for embedded memory designs

General Description

Mirrored DDRII400 Support

Use of this product along with the NEX-DDRII400BM product provides the mechanical clearance necessary to simultaneously monitor two, adjacent DDR sockets. For more information please contact us.

NEX-DDRII400 allows for the acquisition of Address/Command, Read and Write Data of 240-pin, unbuffered or registered DDRII SDRAM DIMMs up to 400 MT/s

8Ghz Timing Analysis available for all DDRII signals

Oscilloscope Connectivity on any channel without re-probing via the TLA's Enhanced iView Analog Mux capability

Selective Clocking stores data when commands are present and for 13 clock cycles after Column Address Assertion. This results in fewer Idle cycles being stored in acquisition memory.

Pre-Defined Symbols for the following Command Cycles allow for easy Trigger Setup:

- Read Col Address Read
- Write Col Address Write
- Mode Register Set
- Row Address Strobe
- Precharge
- Ignore Command Data
- Burst Stop
- Refresh
- Precharge Select Bank
- No Operation

No Dedicated Slot Required – The logic analyzer connects above the normal DIMM height so that there is no interference with adjacent DIMMs.

The following support package(s) are included with this product:

400DDR2S allows the user to acquire DDRII data from a target that uses 18 Data Strokes (16 for Data and 2 for Check Bits) instead of Write Masks. This support requires 7-P6860 and 1-P6880 High-Density Compression probes.

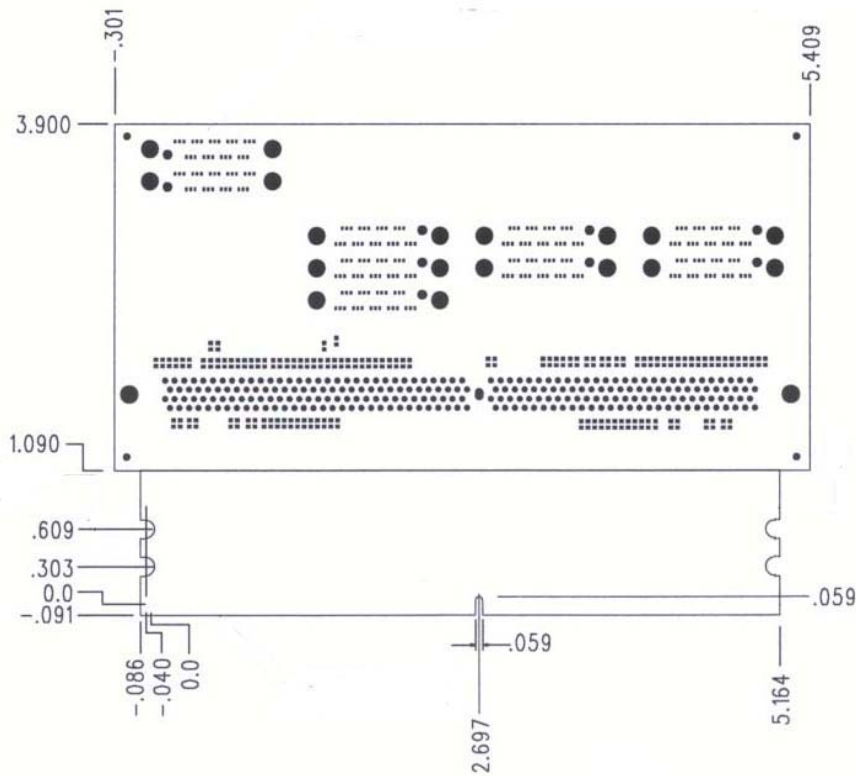
400DDR2M allows the user to acquire DDRII data from a target that uses 8 Data Strokes and 8 Write Masks. This support requires 8-P6860 High-Density Compression probes.

LA Support / Configuration

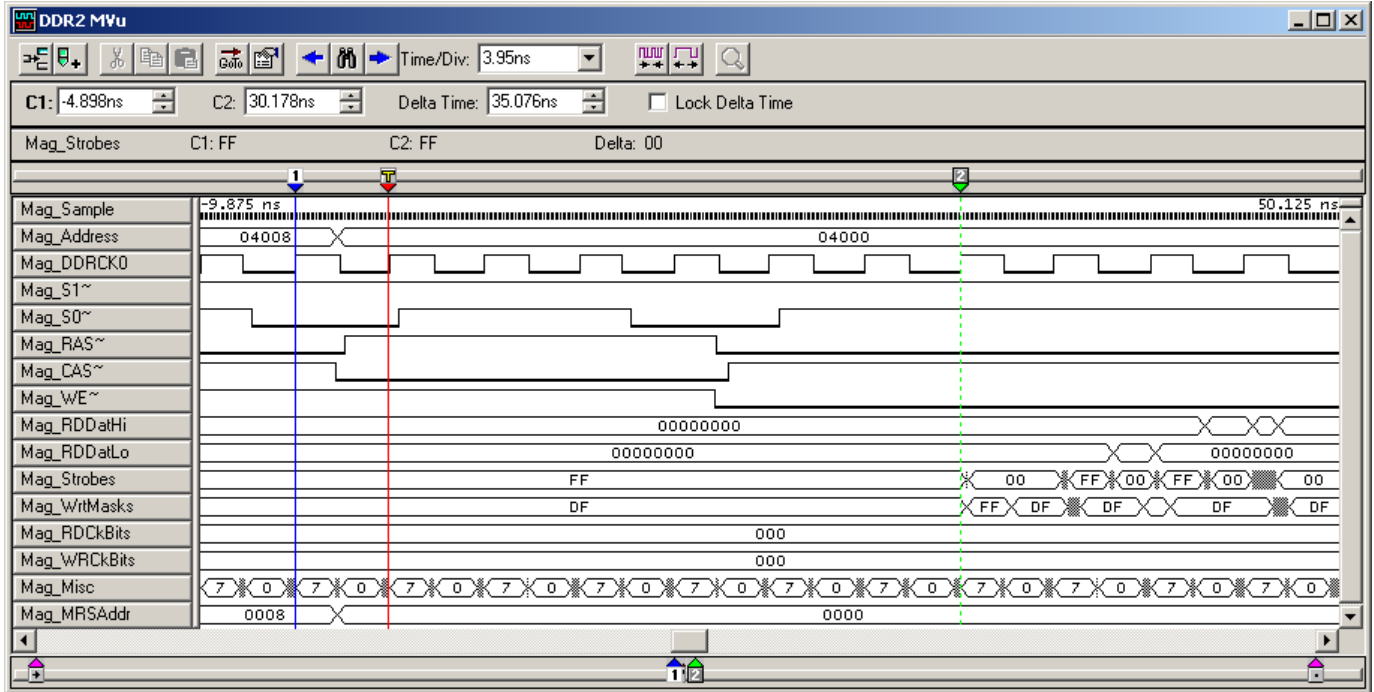
A TLA700 equipped with two, merged, 450MHz state speed acquisition modules (TLA7AA4 or TLA7AB4 cards) are required. When the upper Strobe/Write Mask signals are used as differential strobes (400DDR2S support) 1-P6880 and 7-P6860 probes are required. When the upper Strobe/Write Mask signals are used as Write Masks or single-ended strobes (400DDR2M support) 8-P6860 probes are required.

8GHz MagniVu Timing and Enhanced iView Analog Mux capabilities are available with either configuration.

Mechanical Outline



Timing Display



State Display

Sample	DDR2_400 Address	DDR2_400 RDDatHi	DDR2_400 RDDatLo	DDR2_400 WRDatHi	DDR2_400 WRDatLo	DDR2_400 Mnemonics	DDR2_400 Strobes	DDR2_400 WrtMasks	Timestamp
737	04002	00000000	00000000	-----	-----	READ DATA	FF	DF	12.375 ns
738	04002	00000000	00000000	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	2.625 ns
739	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	2.375 ns
740	04000	00000000	00000000	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	2.625 ns
743	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	7.375 ns
744	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	2.750 ns
745	04000	00000000	00000000	-----	-----	READ DATA	00	FF	2.250 ns
746	04000	00000000	00000000	-----	-----	READ DATA	00	DF	2.625 ns
766	04400	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	5.325,875 us
767	04008	-----	-----	-----	-----	ACTV - ROW ADDRESS STROBE (S0~)	FF	DF	709.829,875 us
768	04002	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	5.000 ns
773	04002	00000000	00000000	-----	-----	READ DATA	FF	DF	12.375 ns
774	04002	00000000	00000000	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	2.625 ns
775	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	2.375 ns
776	04000	00000000	00000000	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	2.750 ns
779	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	7.250 ns
780	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	2.750 ns
781	04000	00000000	00000000	-----	-----	READ DATA	00	FF	2.250 ns
782	04000	00000000	00000000	-----	-----	READ DATA	00	DF	2.625 ns
802	04400	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	19.331,875 us
803	04008	-----	-----	-----	-----	ACTV - ROW ADDRESS STROBE (S0~)	FF	DF	695.873,250 us
804	04002	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	4.875 ns
809	04002	00000000	00000000	-----	-----	READ DATA	FF	DF	12.375 ns
810	04003	00000000	00000000	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	2.750 ns
811	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	2.250 ns
812	04000	00000000	00000000	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	2.750 ns
815	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	7.375 ns
816	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	2.625 ns
817	04000	00000000	00000000	-----	-----	READ DATA	00	FF	2.250 ns
818	04000	00000000	00000000	-----	-----	READ DATA	00	DF	2.750 ns
838	04400	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	12.454,625 us
839	04008	-----	-----	-----	-----	ACTV - ROW ADDRESS STROBE (S0~)	FF	DF	702.753,000 us
840	04002	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	5.000 ns
845	04002	00000000	00000000	-----	-----	READ DATA	FF	DF	12.375 ns
846	04003	00000000	00000000	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	2.625 ns
847	04000	00000000	00000000	-----	-----	READ DATA	FF	DF	2.375 ns
848	04000	00000000	00000000	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	2.625 ns

Software Mode, Address, Data and Command Cycles Only

Ordering / Contact Information

Part Number NEX-DDR11400

Includes: NEX-DDR11400 adapter
Software
Manual

Additional software included:

NEX-DDRSPA - TLA Software Plug-In for determining optimum Setup & Hold sample points.

Postal: Nexus Technology, Inc.
78 Northeastern Blvd. #2
Nashua, NH 03062

Telephone: 877-595-8116

Fax: 877-595-8118

Email: support@nexustechnology.com
quotes@nexustechnology.com
techsupport@nexustechnology.com

Website: www.nexustechnology.com

Placing an Order

Credit Card orders can be placed directly at 877-595-8116.
Purchase orders can be faxed to 877-595-8118.

Nexus Technology, Inc. reserves the right to make changes in design or specification at any time without notice. Nexus Technology, Inc. does not assume responsibility for use of any circuitry described. All trademarks are the property of their respective owners.