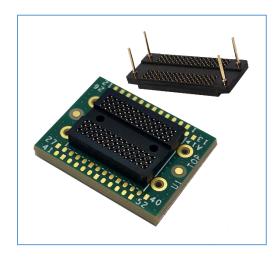


DDR5 Interposers

DDR5 x4/x8 XH Series Socketed Interposer



Key Features

- DDR5
- XH Series Universal Probe Interposer
- x4/x8
- High fidelity
- · Enables oscilloscope probing
- * For DDR5 x4/x8 82 Ball Support, Please Contact Nexus

Applications

- DDR5
 - Memory validation and debug
 - Monitoring bus traffic
 - Bus traffic measurement
 - Analog insight

Socketed Interposer Concept

Target Socketed interposers consists of a target socket, an interposer, and *optionally* a component socket. The target socket has a standard ball-grid-array (BGA) interface which is used to install the socket onto the target. The target socket has a removable socket interface to which the interposer can be mechanically attached and re-attached by using a simple tool and your finger to press-fit the parts together. The target socket's design forces the interposer up and over adjacent components, allowing this solution to fit in a wide variety of mechanically tight applications.

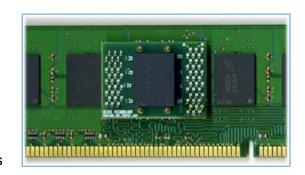
If an optional component socket is provided, this socket will reside on the interposer and provide a mechanically constrained and reusable interface for attaching standard BGA components. If the optional component socket is not provided, the standard BGA component is attached to the interposer using standard BGA component attachment techniques. Lastly, when testing is completed, the interposer can be removed and the BGA component may be press fit directly into the custom socket on the target essentially removing the affect of the interposer in the target.

Simulation and De-Embedding

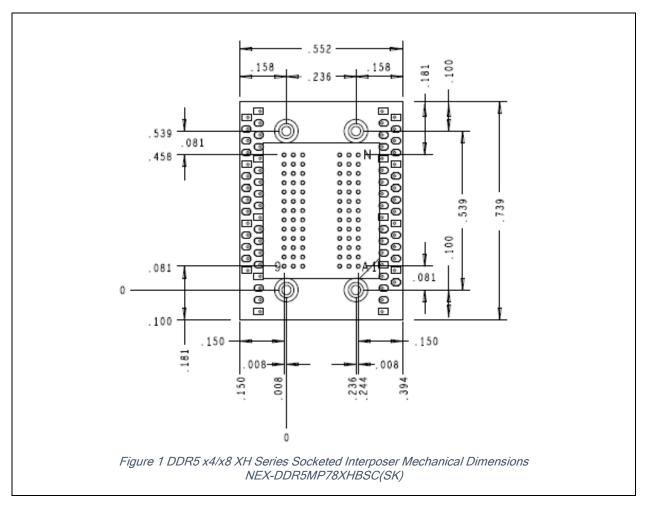
Oscilloscope de-embedding software filters/removes interposer effects. Please contact Nexus for the appropriate de-embedding software for your interposer.

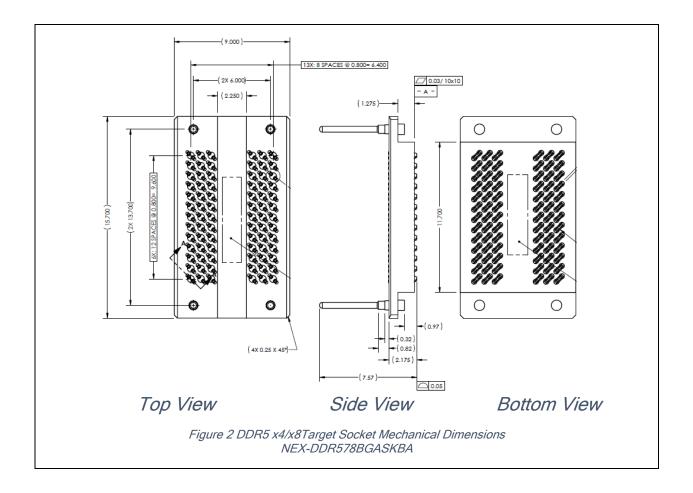
Attachment Service

Nexus Technology's expert attachment service provides a ready-to-go test solution customized to your application. We will attach the interposer and any additional accessories to your application's target. We can also power-on and test your application to confirm functionality. Please contact us for more information.



Mechanical Outline





Probe Point Signal List

TEST Point	Signal	TEST Point	Signal	TEST Point	Signal	TEST Point	Signal
1	GND	14	DQ2	27	GND	40	GND
2	LBDQ	15	DQS_t	28	CA11	41	CA13
3	DQ0	16	DQS_c	29	CA9	42	GND
4	DQ4	17	GND	30	CA3	43	RESET_n
5	MIR	18	DQ6	31	CA1	44	CA7
6	GND	19	CA_ODT	32	GND	45	CA5
7	ALERT_n	20	CS_n	33	CK_C	46	TEN
8	CA4	21	CA0	34	CK_t	47	GND
9	CA6	22	GND	35	DQ7	48	DQ5
10	CAI	23	CA2	36	GND	49	DQ1
11	GND	24	CA8	37	TDQS_c	50	ZQ
12	CA12	25	CA10	38	DM_n/TDQS_t	51	LBDQS
13	GND	26	GND	39	DQ3	52	GND

Probe Point Locations

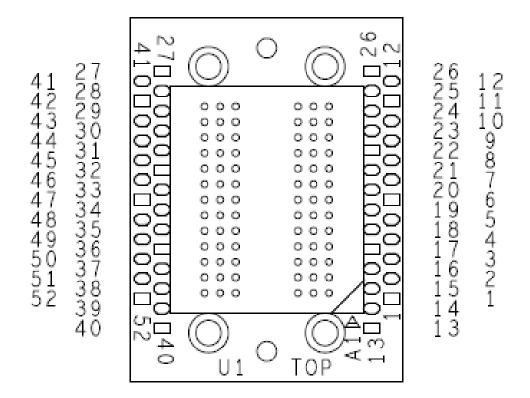


Figure 3 DDR5 x4/x8XH Series Socketed Interposer Probe Point Locations

Product Configuration Table

Nomenclature	Memory Socket	
NEX-DDR5MP78XHBSC	No	
NEX-DDR5MP78XHBSCSK	Yes	

Available Accessories

Туре	Desc.	Nomenclature	
Riser	DDR5 x4/x8 Riser	NEX-RSRDDR578 ¹	
	elevates interposer 0.050"		
Memory Socket	Memory socket	NEX-SOCKETDDR578	
Interposer Socket	Patented interposer socket	NEX-DDR578BGASKBA	

¹ P/N NEX-RSRDDR578 includes solderballs.

Contact Information

For more information, please contact us by telephone, email or mail as listed below. Normal business hours are 9:00 - 5:00 EDT/EST.

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