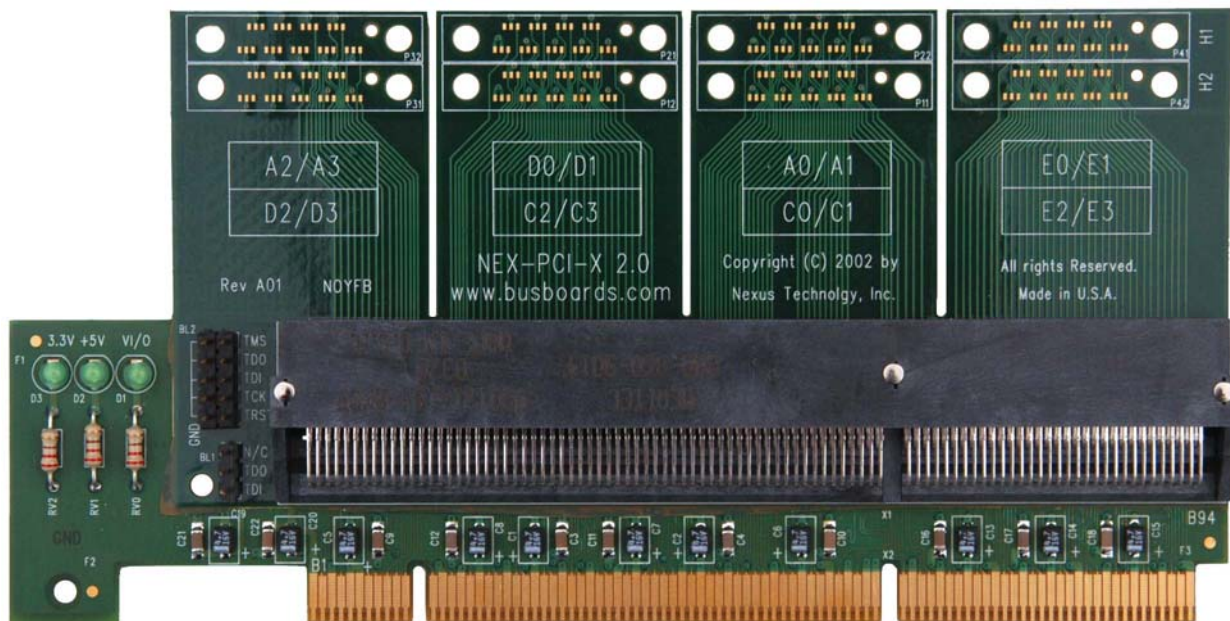


NEX-PCIX266



- Disassembly of the PCI-X 2.0 (266MHz) Bus
- Unique Rigid/Flex design provides superior signal integrity and mechanical clearance
- Controlled Impedance Design
- Matched Signal Length Design
- No Active Buffering of the PCI-X 2.0 Signals
- Support for PCI-X 2.0 at the full 266 MHz speed
- Extender Card Design
- High Density Connectors provide a quick convenient connection to the PCI-X 2.0 Bus
- Timing Analysis to 8 GS/s (125ps) on each channel
- Supports PCI-X 1.0
- Logic Analyzer Setup Software gets you up and running fast
- Simultaneous State and Timing acquisition on each channel
- Trigger on Setup/Hold violations on all channels

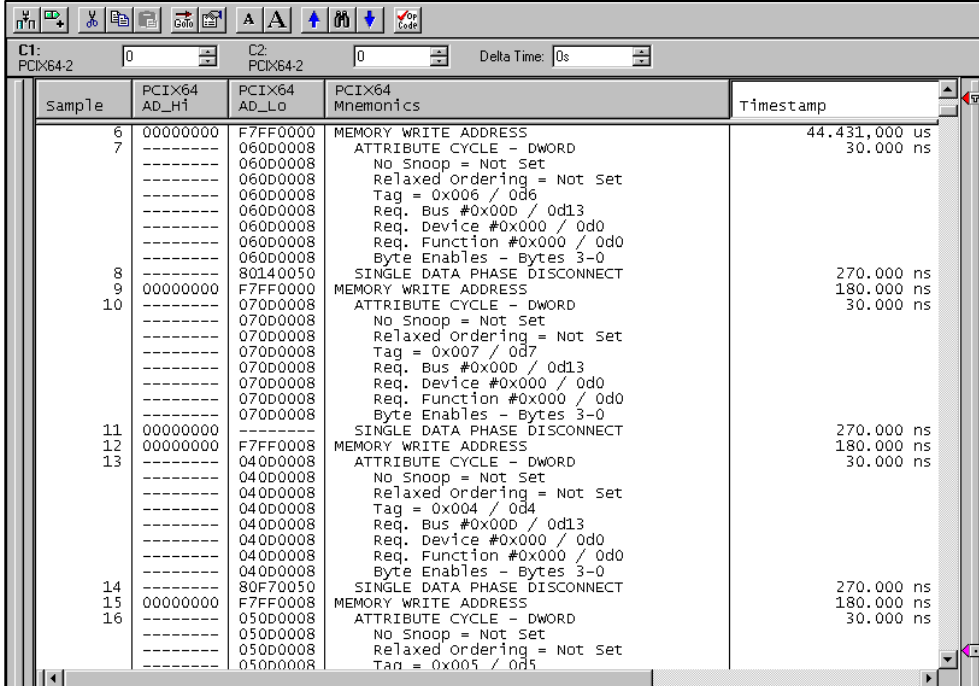
General Description

NEX-PCIX266 Adapter Board

The NEX-PCI-X 2.0 adapter board is a unique rigid/flex/rigid design. The rigid section on the bottom provides a solid connection to the PCI-X 2.0 target. The overlaying flex extends up to the compression pads that are necessary for connection to the Tektronix P6860 probes. The flex can be easily moved to provide mechanical clearance as needed. Please refer to the mechanical outline provided in this data sheet.

PCI-X 2.0 Disassembly Software

The included NEX-PCIX20 disassembly software executes on the Tektronix Logic Analyzer and supports both PCI-X 1.0 (66/133MHz) and PCI-X 266 Mode 2 (PCI-X 266MHz). This software decodes bus transactions and displays information in an easily understood form, just like a typical Tektronix microprocessor disassembler. All PCI-X Cycle types are identified and Config cycles are decoded to reflect the meaning of the registers.



Sample	PCI-X64 AD_Hi	PCI-X64 AD_Lo	PCI-X64 Mnemonics	Timestamp
6	00000000	F7FF0000	MEMORY WRITE ADDRESS	44.431,000 us
7	-----	060D0008	ATTRIBUTE CYCLE - DWORD	30.000 ns
-----	-----	060D0008	No Snoop = Not Set	
-----	-----	060D0008	Relaxed Ordering = Not Set	
-----	-----	060D0008	Tag = 0x006 / 0d6	
-----	-----	060D0008	Req. Bus #0x000 / 0d13	
-----	-----	060D0008	Req. Device #0x000 / 0d0	
-----	-----	060D0008	Req. Function #0x000 / 0d0	
-----	-----	060D0008	Byte Enables - Bytes 3-0	
8	80140050	-----	SINGLE DATA PHASE DISCONNECT	270.000 ns
9	00000000	F7FF0000	MEMORY WRITE ADDRESS	180.000 ns
10	-----	070D0008	ATTRIBUTE CYCLE - DWORD	30.000 ns
-----	-----	070D0008	No Snoop = Not Set	
-----	-----	070D0008	Relaxed Ordering = Not Set	
-----	-----	070D0008	Tag = 0x007 / 0d7	
-----	-----	070D0008	Req. Bus #0x000 / 0d13	
-----	-----	070D0008	Req. Device #0x000 / 0d0	
-----	-----	070D0008	Req. Function #0x000 / 0d0	
-----	-----	070D0008	Byte Enables - Bytes 3-0	
11	00000000	-----	SINGLE DATA PHASE DISCONNECT	270.000 ns
12	00000000	F7FF0000	MEMORY WRITE ADDRESS	180.000 ns
13	-----	040D0008	ATTRIBUTE CYCLE - DWORD	30.000 ns
-----	-----	040D0008	No Snoop = Not Set	
-----	-----	040D0008	Relaxed Ordering = Not Set	
-----	-----	040D0008	Tag = 0x004 / 0d4	
-----	-----	040D0008	Req. Bus #0x000 / 0d13	
-----	-----	040D0008	Req. Device #0x000 / 0d0	
-----	-----	040D0008	Req. Function #0x000 / 0d0	
-----	-----	040D0008	Byte Enables - Bytes 3-0	
14	80F70050	-----	SINGLE DATA PHASE DISCONNECT	270.000 ns
15	00000000	F7FF0000	MEMORY WRITE ADDRESS	180.000 ns
16	-----	050D0008	ATTRIBUTE CYCLE - DWORD	30.000 ns
-----	-----	050D0008	No Snoop = Not Set	
-----	-----	050D0008	Relaxed Ordering = Not Set	
-----	-----	050D0008	Tag = 0x005 / 0d5	

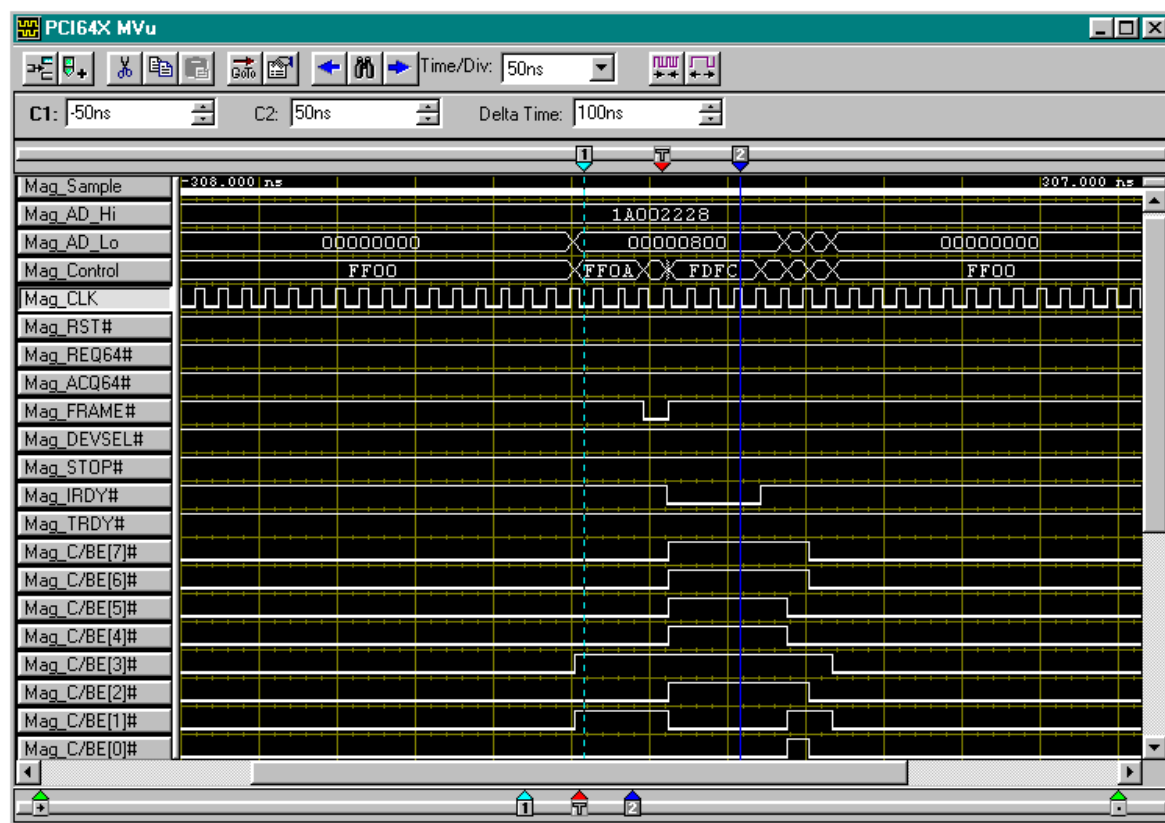
Sample PCI-X Disassembly Screen

It is also possible to filter the data display to show only those cycle types of interest. The user can choose to display or suppress Memory, I/O, or Config cycles to permit easy and quick analysis of only those cycles of interest.

Another feature of the disassembly software is its ability to intelligently acquire PCI-X data. By taking advantage of the data clocking power built in to the Tektronix Logic Analyzers the disassembly software is able to acquire only the PCI-X bus cycles and ignore Idle and Wait states. This means that the user is able to make optimum use of the acquisition card's memory and see more bus transactions. For debug purposes the user also has the ability to override this function and acquire data on every PCI-X CLK rising edge to permit the user to see all of the bus traffic including the Idle and Wait states.

Timing Analysis

Timing analysis of the PCI-X bus can be done at up to 8Ghz (125ps) on each channel. Fast timing acquisition, and a PCI-X 2.0 adapter designed to provide low capacitive loading, controlled impedance, matched signal length and no active buffering results in excellent timing analysis on each channel of the PCI-X bus.



Tektronix Logic Analyzers Supported

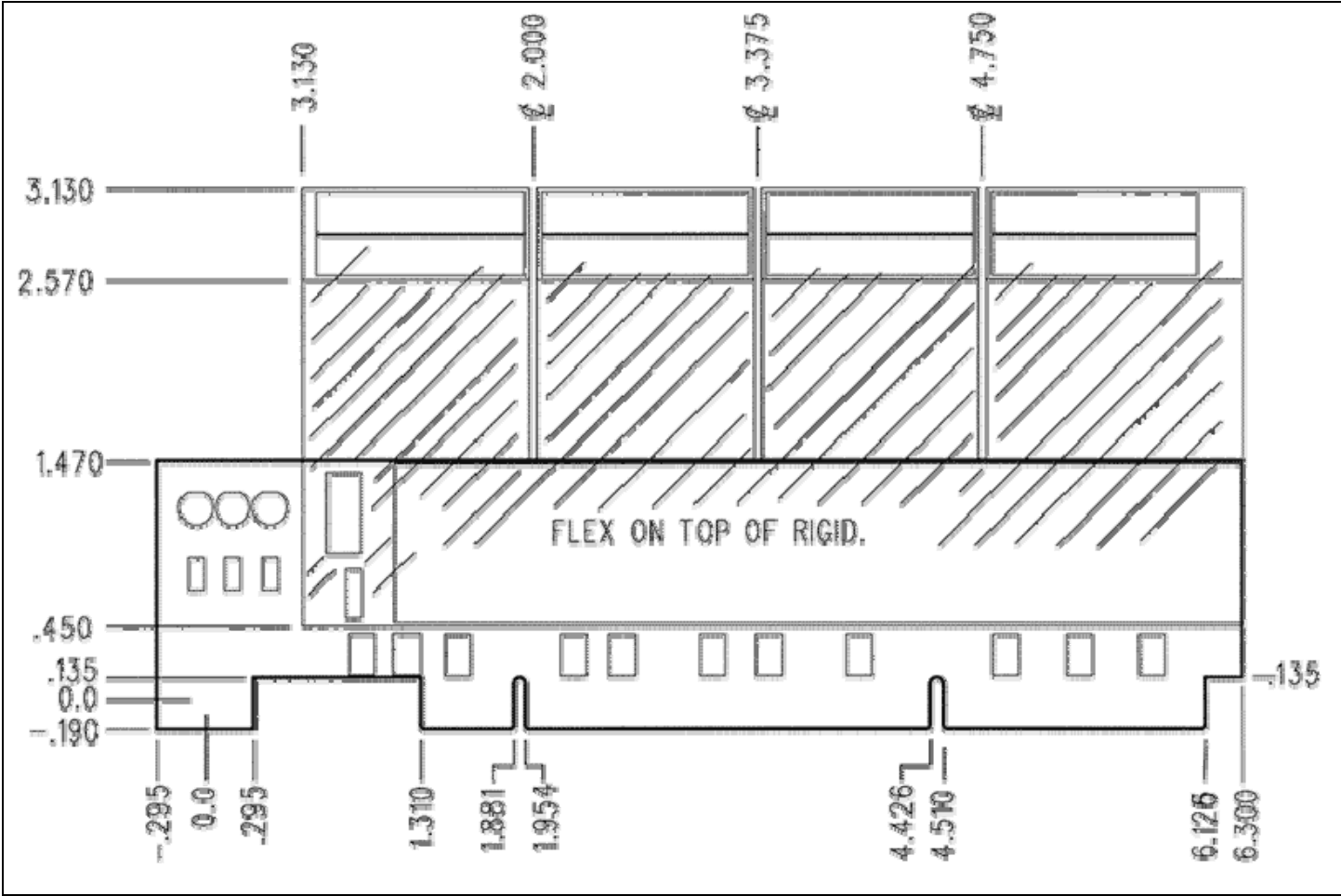
PCI-X 2.0 266MHz

All Tektronix TLA700 series Logic Analyzers with a TLA7Ax4 acquisition module: 136 channel, 450 MHz sync. acquisition for PCI-X 2.0 266MHz. Four P6860 probes are also required and are available from Tektronix.

PCI-X 1.0 66/133MHz

All Tektronix TLA700 series Logic Analyzers with a TLA7Ax4 acquisition module: 136 channel, 235 MHz sync. (or greater) acquisition for PCI-X 133MHz. Four P6860 probes are also required and are available from Tektronix.

Mechanical Outline



Ordering / Contact Information

Part Number NEX-PCIX266B

Includes: NEX-PCI-X 2.0 266MHz Bus Adapter
Software
Manual

Postal: Nexus Technology, Inc.
78 Northeastern Blvd. #2
Nashua, NH 03062

Telephone: 877-595-8116

Fax: 877-595-8118

Email: support@nexustechology.com
quotes@nexustechology.com
techsupport@nexustechology.com

Website: www.nexustechology.com

Placing an Order

Credit Card orders can be placed directly at 877-595-8116.
Purchase orders can be faxed to 877-595-8118.

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