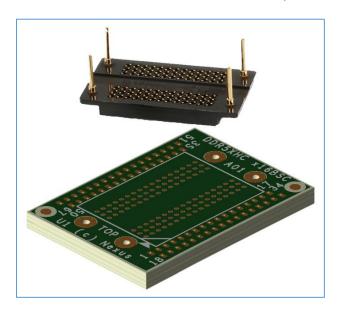


DDR5 Interposers

DDR5 x16 XH Series Socketed Interposer



Key Features

- DDR5
- XH Series Universal Probe Interposer
- x16
- High fidelity
- Enables oscilloscope Probing
- * For DDR5 x16 106 Ball Support, Please Contact Nexus.

Applications

- DDR5
 - Memory validation and debug
 - Monitoring bus traffic
 - Bus traffic measurement
 - Analog insight

Socketed Interposer Concept

Target Socketed interposers consists of a target socket, an interposer, and *optionally* a component socket. The target socket has a standard ball-grid-array (BGA) interface which is used to install the socket on to the target. The target socket has a removable socket interface to which the interposer can be mechanically attached and re-attached by using a simple tool and your finger to press-fit the parts together.

The target socket's design forces the interposer up and over adjacent components, allowing this solution to fit in a wide variety of mechanically tight applications.

If an optional component socket is provided, this socket will reside on the interposer and provide a mechanically constrained and reusable interface for attaching standard BGA components. If the optional component socket is not provided, the standard BGA component is attached to the interposer using standard BGA component attachment techniques. Lastly, when testing is completed, the interposer can be removed and the BGA component may be press fit directly into the custom socket on the target essentially removing the affect of the interposer in the target.

Simulation and De-Embedding

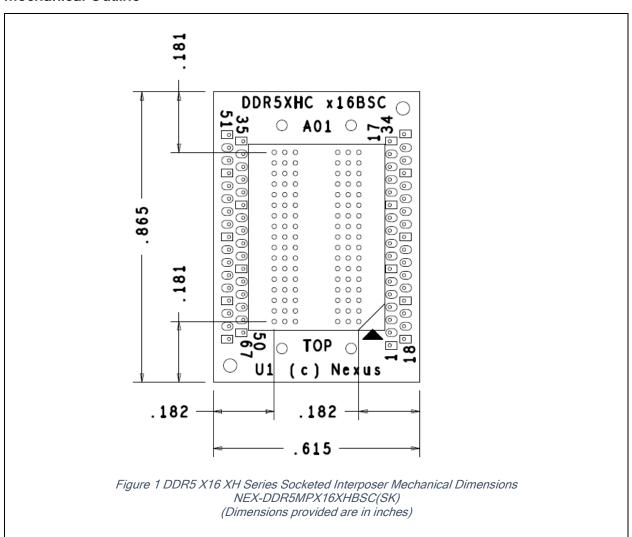
Oscilloscope de-embedding software filters/removes interposer effects. Please contact Nexus for the appropriate de-embedding software for your interposer.

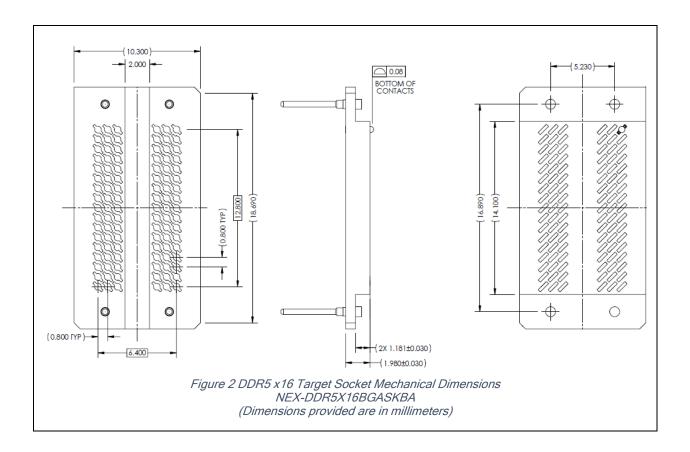
Attachment Service

Nexus Technology's expert attachment service provides a ready-to-go test solution customized to your application. We will attach the interposer and any additional accessories to your application's target. We can also power-on and test your application to confirm functionality. Please contact us for more information.



Mechanical Outline





Probe Point Signal List

TEST	Signal	TEST	Signal	TEST	Signal	TEST	Signal
Point		Point		Point		Point	
1	GROUND	18	GROUND	35	GROUND	52	CA11
2	DQU2(DQ10)	19	LBDQ	36	CA13	53	RESET_n
3	DQSU_t	20	DQU0(DQ8)	37	CA9	54	GROUND
4	DQSU_c	21	GROUND	38	CA3	55	CA7
5	DQU6(DQ14)	22	DQU4(DQ12)	39	CA1	56	CA5
6	DQL2(DQ2)	23	DQL0(DQ0)	40	GROUND	57	TEN
7	GROUND	24	DQL4(DQ4)	41	CK_c	58	DQL5(DQ5)
8	DQSL_t	25	MIR	42	CK_t	59	GROUND
9	DQSL_c	26	GROUND	43	DQL7(DQ7)	60	DQL1(DQ1)
10	DQL6(DQ6)	27	CA_ODT	44	RFU_H7	61	DQU5(DQ13)
11	CS_n	28	ALERT_n	45	GROUND	62	DQU1(DQ9)
12	GROUND	29	CA4	46	DML_n	63	LBDQS
13	CA0	30	CA6	47	DQL3(DQ3)	64	GROUND
14	CA2	31	GROUND	48	DQU7(DQ15)	65	RFU_D7
15	CA8	32	CAI	49	DMU_n	66	DQU3(DQ11)
16	CA12	33	CA10	50	GROUND	67	GROUND
17	GROUND	34	GROUND	51	GROUND		

Probe Point Locations

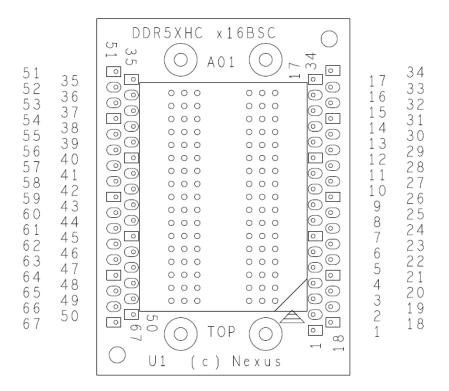


Figure 3 DDR5 x16 XH Series Socketed Interposer Probe Point Locations

Product Configuration Table

Nomenclature	Memory	
	Socket	
NEX-DDR5MPX16XHBSC	No	
NEX-DDR5MPX16XHBSCSK	Yes	

Available Accessories

Туре	Desc.	Nomenclature
Riser	DDR5 x16 Riser	NEX-RSRDDR5X16 ¹
	elevates interposer 0.050"	
Memory Socket	Memory socket	NEX-SOCKETDDR5X16
Interposer Socket	Patented interposer socket	NEX-DDR5X16BGASKBA

¹ P/N NEX-RSRDDR5X16 includes solderballs.

Contact Information

For more information, please contact us by telephone, email or mail as listed below. Normal business hours are 9:00 - 5:00 EDT/EST.

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