

# DDR5 Analyzer Interposer

x4/x8 BGA Component | Direct Attach | Analyzer

## OVERVIEW

The DDR5-D-CDA-78 Direct Attach BGA interposer enables probing of DDR5 x4/x8 components with an MA5100 series analyzer.

- 78-/82-ball DDR5 x4/x8 BGA Component
- JEDEC Standard JESD79-5
- Easily access signals
- Small keep-out volume (KOV)
- Riser Not Needed
- S-Parameters for System Simulation
- Installs with standard BGA Rework
- Attachment and Analysis Services Available

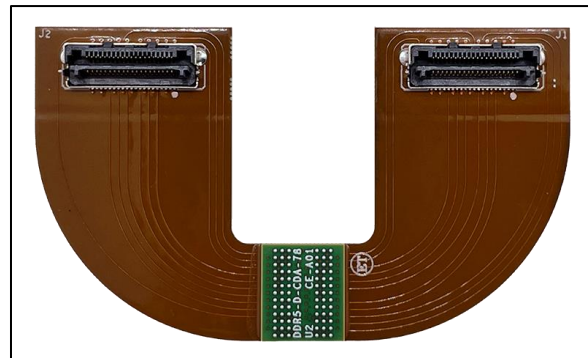


Figure 1 DDR5-D-CDA-78

## BGA INTERPOSER ADVANTAGES

The DDR5-D-CDA-78 interposer enables high speed probing at with an MA5100 series analyzer. The interposer is easy to install using standard BGA rework practices. Or take advantage of Nexus' attachment and testing services.

## DIRECT ATTACH – CONVENIENT AND ACCURATE

Optimal DDR5 validation requires analysis of the DDR5 signals as seen by the memory components. This allows for the highest confidence that the signals captured are representative, contain little interference, and present the maximum possible data eye size.

## ATTACHMENT AND TESTING SERVICES

The following services are available for this interposer.

### ATTACHMENT SERVICE

For each interposer, the *Attachment Service* will install a component to the interposer and the interposer to the target. Other accessories may also be attached (risers, sockets, etc.).

### TARGET BOOT & BASIC FUNCTIONALITY

For each target, this service will power on and ensure functionality of the target with the interposer(s) installed.

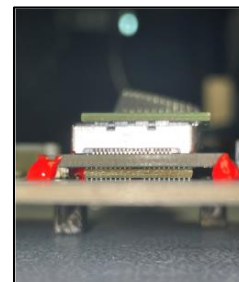


Figure 2 Attachment Service Example

This service will power on the target and place the target in a predefined state. A MA5150 analyzer will then be connected to the target and the following tests will be performed and the following reports will be provided.

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The screenshot shows the MAX100 software interface. The top menu bar includes File, Setup, Sessions, System, Tools, Window, and Help. Below the menu is a toolbar with icons for various functions. The main window is divided into two panes. The left pane, titled '(S14.1) SREF: Acquisition Listing Filter: Command0', displays a table of timing set configurations. The right pane, titled '(S14.1) SREF: Raw Statistics', displays a table of raw statistics for the selected configuration. The raw statistics table includes counts for various system and timing parameters.

Elapsed	Command Rank 0	Command Rank 1
0.63 ms	ACT	DES
0.63 ms	-CS01 BA:4	DES
0.63 ms	-R0x0FFFF	DES
0.63 ms	-END	DES
0.63 ms	DES	ACT
0.63 ms	DES	-CS01 BA:4
0.63 ms	DES	-R0x0FFFF
0.63 ms	DES	-END
14.38 ms	ADICIT Disabled	DES
0.63 ms	-CS01 BA:4	DES
0.63 ms	-C0x01F0	DES
0.63 ms	-END	DES
24.38 ms	DES	ADICIT Disabled
0.63 ms	-CS01 BA:4	DES
0.63 ms	DES	-C0x01F0

System	Value
sBankActive Count	00
sBankIdle Count	00
sBankActive Count	00
sBankRef Count	00
SRE Count	00
sRefOrder Count	00
SRX Count	00
tCCMn Count	00
tCCMnWn Count	00
tCCKCHMD Count	00
tCCKLPD Count	00
tESCKe Count	00
tFAN Count	00
tEND Count	00

Session Run Time: 1.438 ms or 0.00

Export...

Session % Step %

## SIGNALS PROBED

- |   |        |   |        |
|---|--------|---|--------|
| - | ALERT# | - | CA0    |
| - | CA0    | - | CA0    |
| - | CA0    | - | CA0    |
| - | CA0    | - | CA0    |
| - | CA0    | - | CAI    |
| - | CA0    | - | CLK+   |
| - | CA0    | - | CLK-   |
| - | CA0    | - | CS#    |
| - | CA0    | - | MIR    |
| - | CA0    | - | RESET# |
| - | CA0    |   |        |

# DIMENSION DRAWINGS

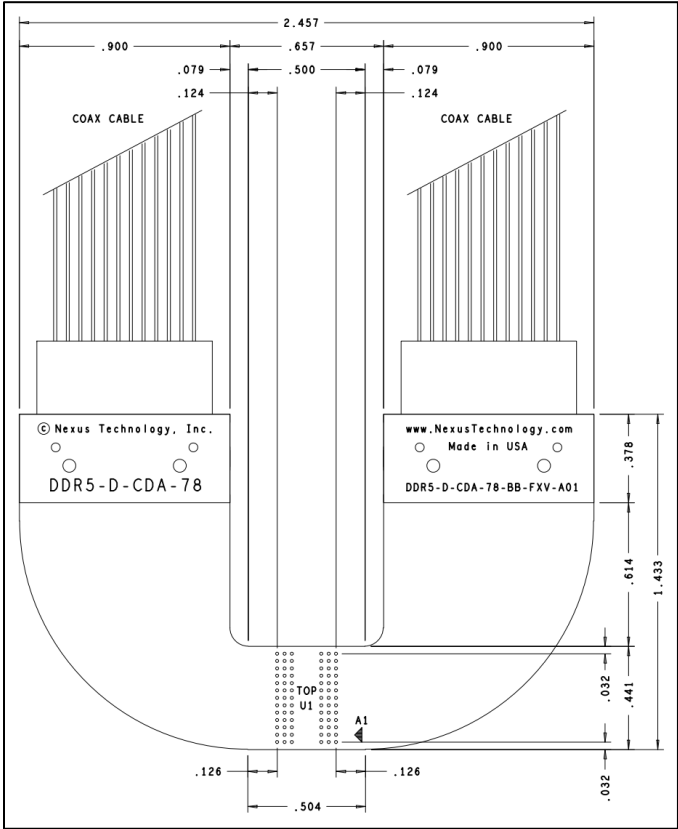


Figure 5 Interposer Mechanicals - Front

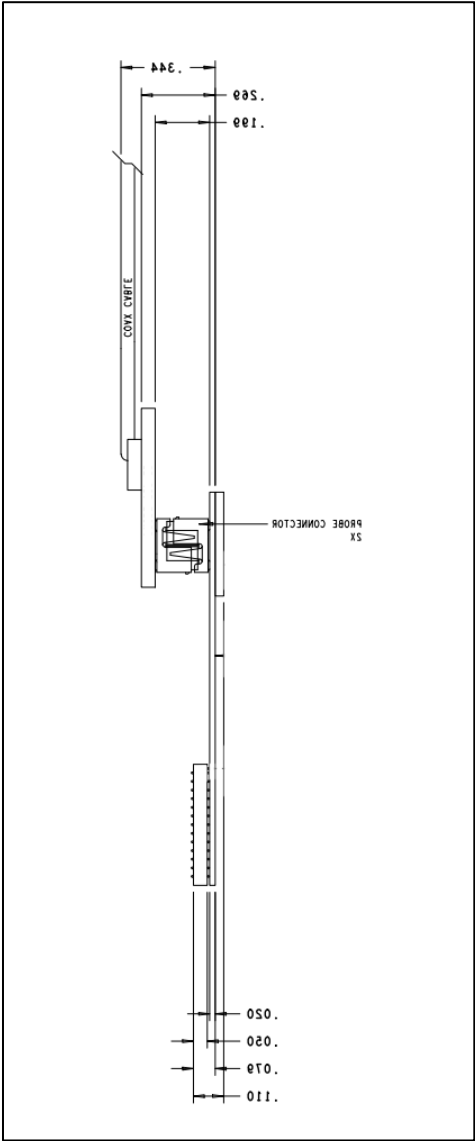


Figure 6 Interposer Mechanicals – Side

# PRODUCT CONFIGURATION TABLE

Product Name	Description	Nexus Order Number
DDR5-D-CDA-78	DDR5 78-ball component interposer for use with MA5100 series analyzers and PRB-HD series probe.	NEX-DDR5-D-CDA-78-V



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